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With its high-capacity FPGA (Xilinx part number XC7A35T-1CPG236C http://www.xilinx.com/support/documentation/data_sheets/ds181_Artix_7_Data_Sheet.pdf), low overall cost, and collection of USB, VGA, and other ports, the Basys3 can host designs ranging from introductory combinational circuits to complex sequential circuits like embedded processors and controllers. It includes enough switches, LEDs and other I/O devices to allow a large number designs to be completed without the need for any ...

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peripherals, students gain exposure to a wide range of embedded systems related concepts while using a professional grade tool set.

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Note for Advanced Users: All demos for the Basys 3 are provided through the Basys-3 repository on Github.

Further documentation on the structure of this repository can be found on this wiki's Digilent FPGA Demo Git Repositories page.

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Basys MX3 features the PIC32MX370 from Microchip and was designed to be used with the MPLAB® X IDE.

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Figure 1. Basys 3 FPGA board with callouts. Table 1. Basys 3 Callouts and

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component descriptions. A growing collection of board support IP, reference designs, and add-on boards are available on the Digilent website. See the Basys 3 page at www.digilentinc.com for more information. 1 Power Supplies

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Basys3 ~~FPGA Board Reference Manual~~ 2 FPGA Configuration After power-on, the Artix-7 FPGA must be configured (or programmed) before it can perform any functions. You can configure the FPGA in one of three ways: 1. A PC can use the Digilent USB-JTAG circuitry (portJ4, labeled ~~PROG~~) to program the FPGA any time the power is on. 2.

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3. A programming file can be transferred from a USB memory stick attached to the USB HID port. Figure 3 shows the different options available for configuring the FPGA. An on-board `mode` jumper (JP1) selects between the programming modes. The FPGA configuration data is stored in files called bitstreams that have the .bit file extension. The ...

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* Only available with Vivado Design Edition - Additional Documentation can be found on the Digilent resource center for Basys 3. Connector(s): USB A; USB micro-B; Four 12-pin Pmod ports; VGA; Features: Features the Xilinx Artix-7 FPGA: XC7A35T-1CPG236C; 33,280 logic cells in 5200 slices (each slice contains four 6-input LUTs and 8 flip-flops)

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The Basys 3 is an entry-level FPGA development board designed exclusively for Vivado Design Suite, featuring Xilinx Artix-7 FPGA architecture. Basys 3 is the newest addition to the popular Basys line of FPGA development boards, and is

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perfectly suited for students or beginners just getting started with FPGA technology.

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