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(AlphabeticalOrder) Instruction
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26 Microcontroller Theory and
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that the stack is a LIFO (last in,

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firstout) memory. As mentioned earlier, a stack is typically used during subroutine CALLs. The CPU automatically PUSHes the return address onto a stack after executing a subroutine CALL instruction in the main program.

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Applications with the PIC18F
TABLE 24-1: OPCODE FIELD DESCRIPTIONS

Field Description	a	RAM access bit	a = 0	a = 1
RAM location in Access RAM (BSR register is ignored)	0	RAM location in Access RAM (BSR register is ignored)	RAM location in Access RAM (BSR register is ignored)	RAM bank is specified by BSR register
Bit address within an 8-bit file register (0 to 7)	bbb	Bit address within an 8-bit file register (0 to 7)	Bit address within an 8-bit file register (0 to 7)	Bit address within an 8-bit file register (0 to 7)

BSR Bank Select Register.

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